

+

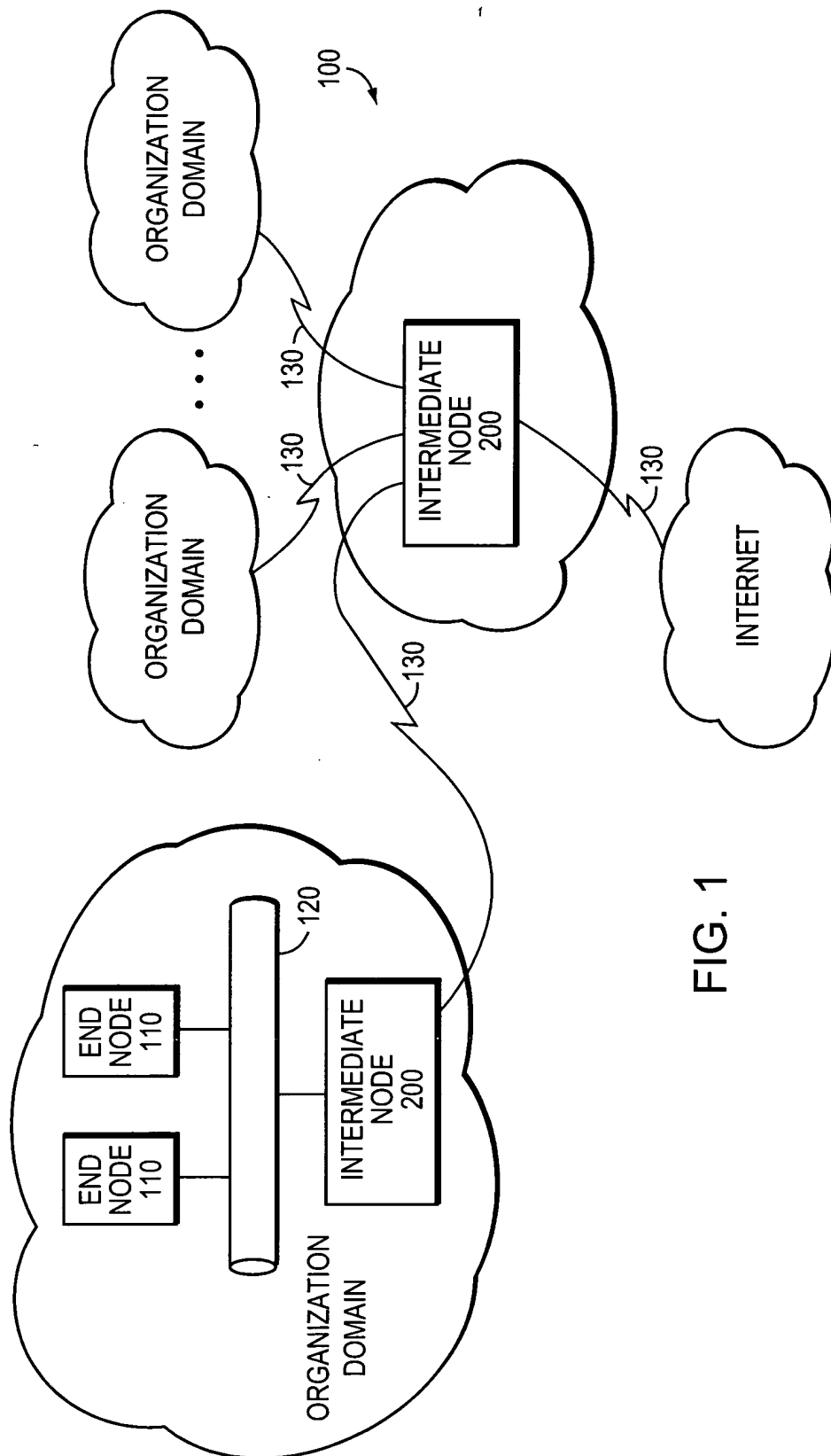
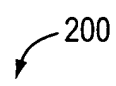


FIG. 1

+



+

100560 = 100000

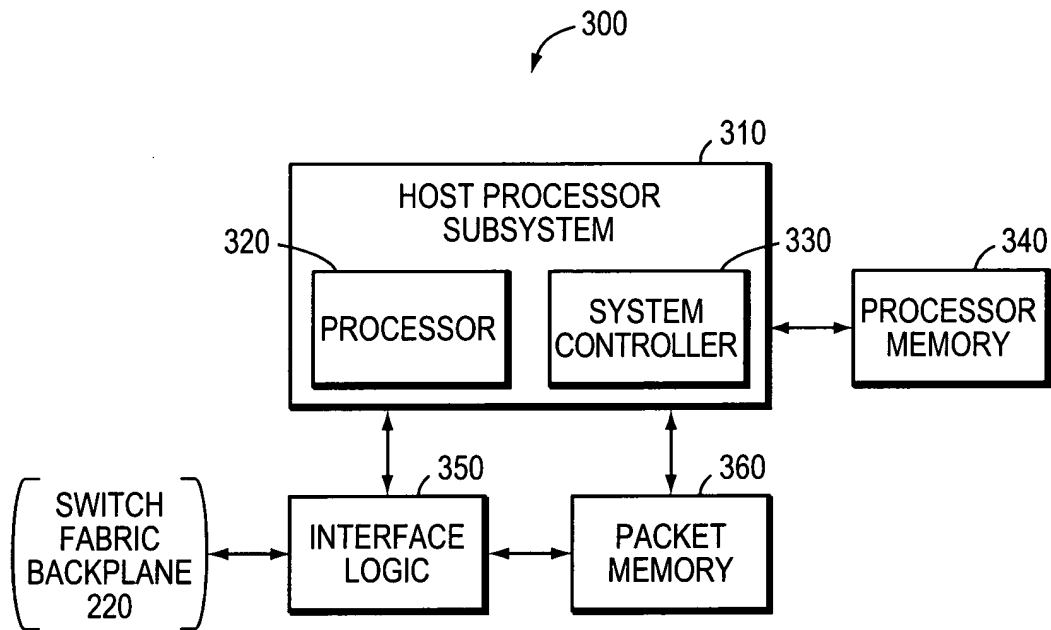


FIG. 3

FIG. 3 is a block diagram of a system architecture.

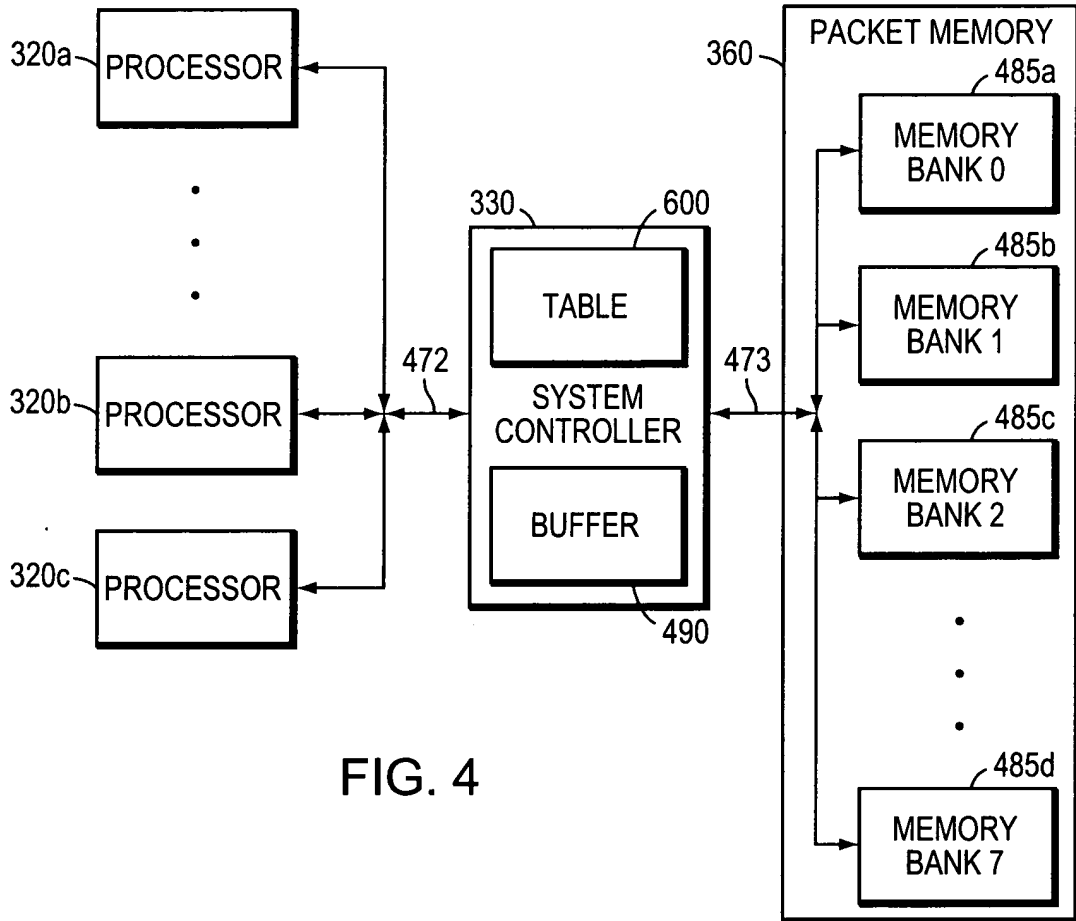


FIG. 4

FIG. 4

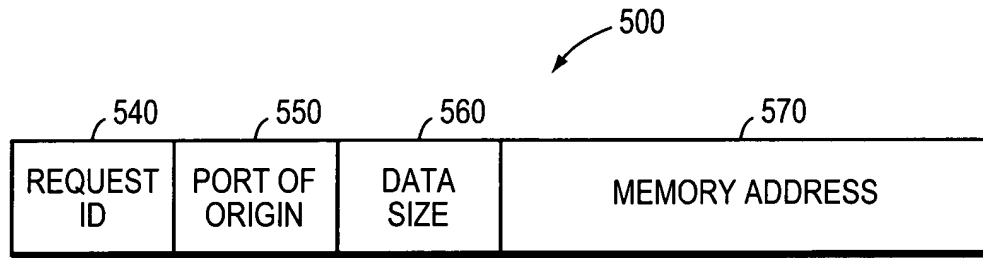


FIG. 5

1003609.13204
Project 5095001

600

640	650	660	670	
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	61
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	

FIG. 6